



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Docket No. 13454US03

In the Application of:

ESIN TERZIOGLU, et al.

U.S. Serial No.: 10/646,317

Filed: August 22, 2003

For: DIFFUSION REPLICA DELAY
CIRCUIT

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 24, 2003.

By: Ronald E. Larson
Ronald E. Larson
Reg. No. 24,478

**TRANSMITTAL OF
INFORMATION DISCLOSURE STATEMENT
AFTER APPLICATION FILING DATE**

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

ENCLOSURES

Enclosed are the following:

This Information Disclosure Statement and a completed PTO Form 1449 (in duplicate).

No copies of the listed references are enclosed since the eighteen (18) cited references were previously provided in the parent case, Serial No. 09/776,029 (U.S. Patent No. 6,611,465).

FEE DETERMINATION

No fee is believed to be due because:

The applicants believe that this statement and enclosures are being filed before the first Office action on the merits has been mailed by the PTO. The basis of this belief is that no Office action on the merits appears to have been received by the undersigned to date.

The IDS is also being sent within three (3) months of the filing date of this application.

The Commissioner is hereby authorized to charge any additional fees which are presently required, or credit any overpayment, to Deposit Account No. 13-0017.

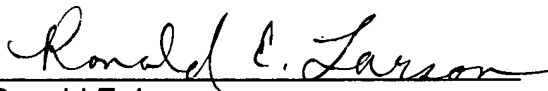
REQUEST FOR CONSIDERATION

This paper and enclosures are believed to be entitled to consideration under 37 C.F.R. § 1.97, based on the facts stated above. The Examiner is required to initial both copies of the enclosed PTO-1449 (modified) and return one copy to the applicant(s) to indicate consideration of the references in this application.

This submission is in no way intended as an admission that the cited references constitute prior art under any subsection of 35 U.S.C. § 102. Applicant expressly retains the right to take any actions necessary to remove the cited references from the available prior art.

Respectfully submitted,

November 24, 2003
DATE



Ronald E. Larson
Reg. No. 24,478
Attorney for applicant(s)

McAndrews, Held & Malloy, Ltd.
34th Floor
500 West Madison Street
Chicago, IL 60661



FORM PTO-1449	SERIAL NO. 10/646,317	CASE NO. 13454US03
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE August 22, 2003	GROUP ART UNIT
(use several sheets if necessary)	APPLICANT(S):	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,170,375	12/1992	Mattausch et al.	365/230.03	
	A2	5,752,264	05/1998	Blake et al.	711/144	
	A3	5,781,498	07/1998	Suh	365/230.06	
	A4	5,864,497	01/1999	Suh	365/72	
	A5	6,040,999	03/2000	Hotta et al.	365/200	
	A6	6,141,286	10/2000	Vo et al.	365/230.03	
	A7	6,141,287	10/2000	Mattausch	365/230.03	
	A8	6,144,604	11/2000	Haller et al.	365/221	
	A9	6,154,413	11/2000	Longwell et al.	365/230.03	
	A10	6,163,495	12/2000	Ford et al.	365/230.03	
	A11	6,166,942	12/2000	Vo et al.	365/63	
	A12	6,166,986	12/2000	Kim	365/230.06	
	A13	6,166,989	12/2000	Hamamoto et al.	365/233	
	A14	6,169,701	01/2001	Eto et al.	365/230.06	
	A15	6,173,379	01/2001	Poplingher et al.	711/165	
	A16	5,936,905	08/1999	Proebsting	365/208	
	A17	6,147,893	11/2000	Liu	365/104	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A18	KIYOO ITOH et al., "Trends in Low-Power RAM Circuit Technologies," Proceedings of the IEEE, April 1995, pp. 524-543, Vol. 83, No. 4, IEE
	A19	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.